

# **EAGLE40-03 HiSecOS Rel. 05101**

**Reference Manual**  
Graphical User Interface

**User Manual**  
Configuration

























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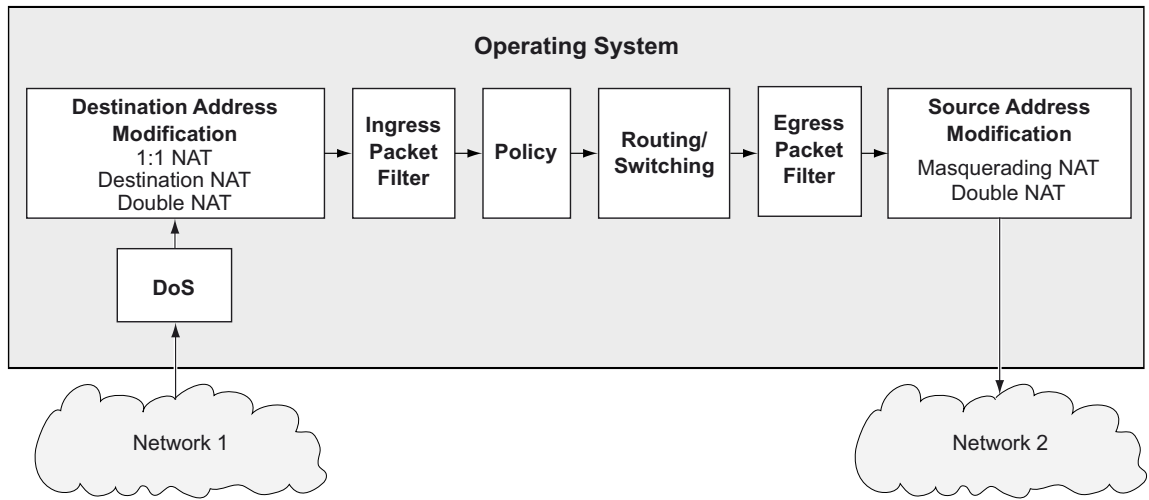
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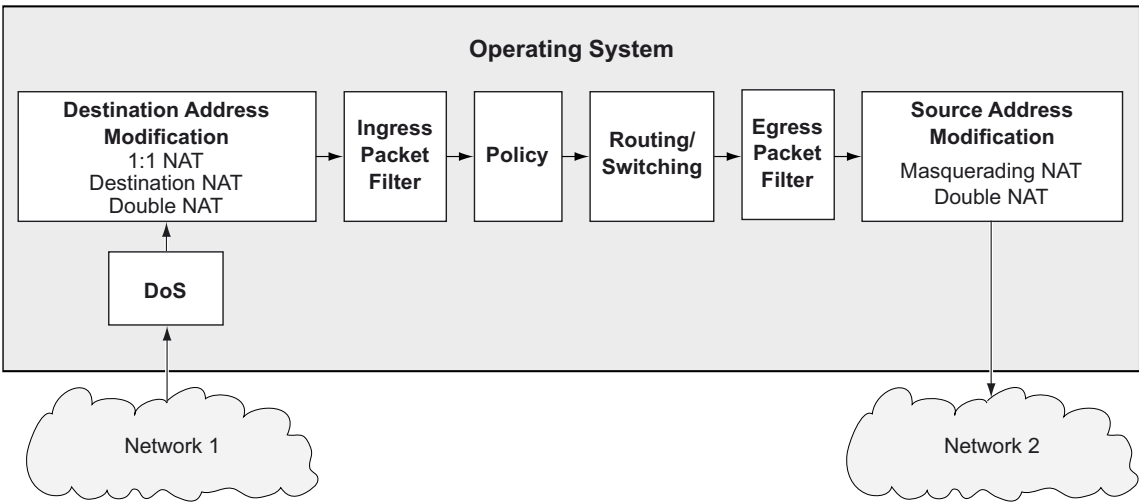


















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A series of horizontal lines for writing, starting below the blue bar and extending to the bottom of the page.





















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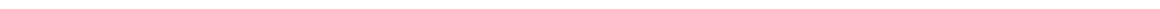
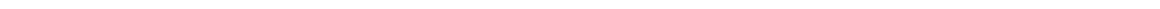
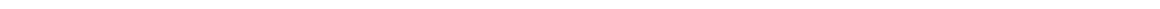


A series of horizontal lines for writing, consisting of 18 lines in total. The first line is immediately below the blue shaded box, and the remaining 17 lines are spaced evenly down the page.



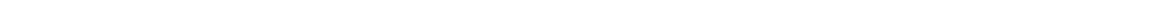


A series of horizontal lines for writing, starting below the blue header bar and extending to the bottom of the page.











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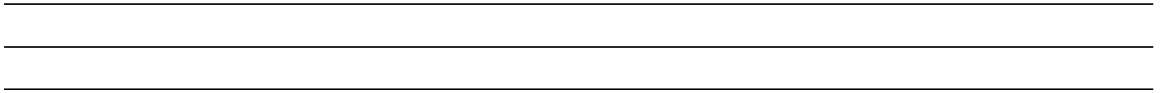
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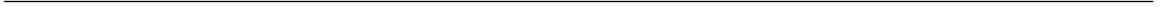
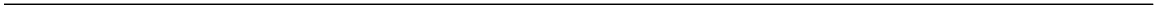
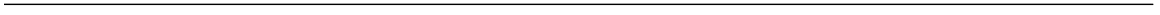
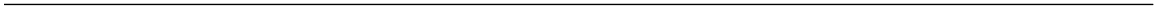
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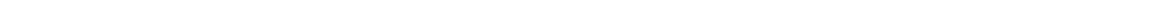
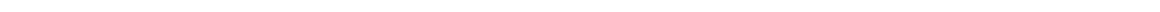
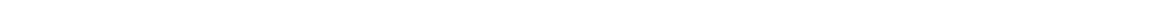
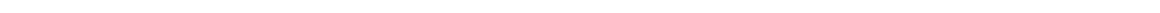
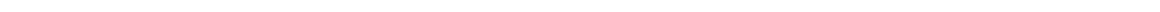


Lined writing area consisting of 24 horizontal black lines.



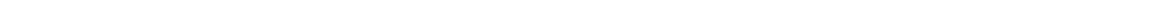
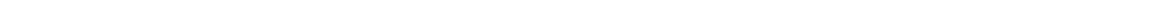
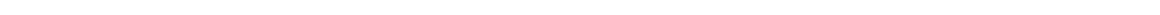
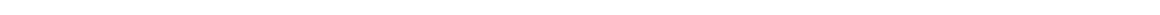


Lined writing area consisting of 21 horizontal black lines.





A series of 22 horizontal lines for writing, spaced evenly down the page.







Multiple horizontal lines for writing.



A series of 28 horizontal black lines for writing, starting below the blue bar and extending to the bottom of the page.

































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A series of horizontal lines for writing, organized into several sections. The first section consists of 10 lines. The second section consists of 10 lines. The third section consists of 10 lines. The fourth section consists of 10 lines. The fifth section consists of 10 lines. The sixth section consists of 10 lines. The seventh section consists of 10 lines. The eighth section consists of 10 lines. The ninth section consists of 10 lines. The tenth section consists of 10 lines. The eleventh section consists of 10 lines. The twelfth section consists of 10 lines. The thirteenth section consists of 10 lines. The fourteenth section consists of 10 lines. The fifteenth section consists of 10 lines. The sixteenth section consists of 10 lines. The seventeenth section consists of 10 lines. The eighteenth section consists of 10 lines. The nineteenth section consists of 10 lines. The twentieth section consists of 10 lines. The twenty-first section consists of 10 lines. The twenty-second section consists of 10 lines. The twenty-third section consists of 10 lines. The twenty-fourth section consists of 10 lines. The twenty-fifth section consists of 10 lines. The twenty-sixth section consists of 10 lines. The twenty-seventh section consists of 10 lines. The twenty-eighth section consists of 10 lines. The twenty-ninth section consists of 10 lines. The thirtieth section consists of 10 lines. The thirty-first section consists of 10 lines. The thirty-second section consists of 10 lines. The thirty-third section consists of 10 lines. The thirty-fourth section consists of 10 lines. The thirty-fifth section consists of 10 lines. The thirty-sixth section consists of 10 lines. The thirty-seventh section consists of 10 lines. The thirty-eighth section consists of 10 lines. The thirty-ninth section consists of 10 lines. The fortieth section consists of 10 lines. The forty-first section consists of 10 lines. The forty-second section consists of 10 lines. The forty-third section consists of 10 lines. The forty-fourth section consists of 10 lines. The forty-fifth section consists of 10 lines. The forty-sixth section consists of 10 lines. The forty-seventh section consists of 10 lines. The forty-eighth section consists of 10 lines. The forty-ninth section consists of 10 lines. The fiftieth section consists of 10 lines.







Lined paper with a blue header bar and horizontal ruling lines.



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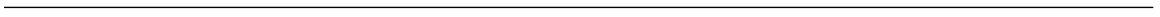
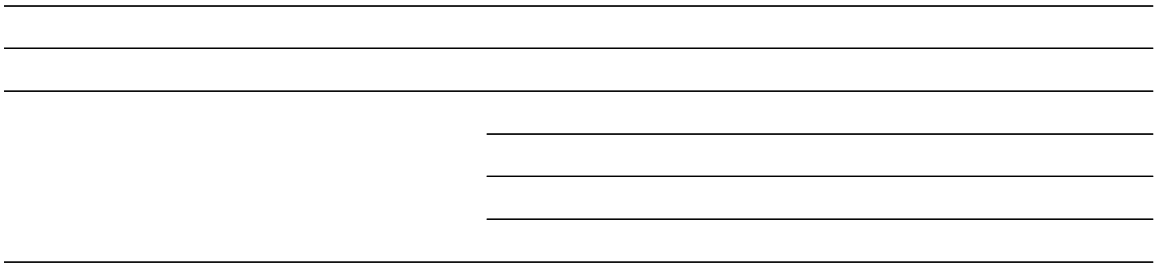
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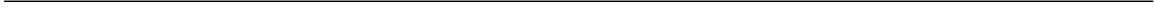
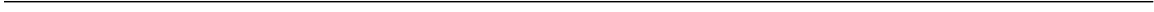
















































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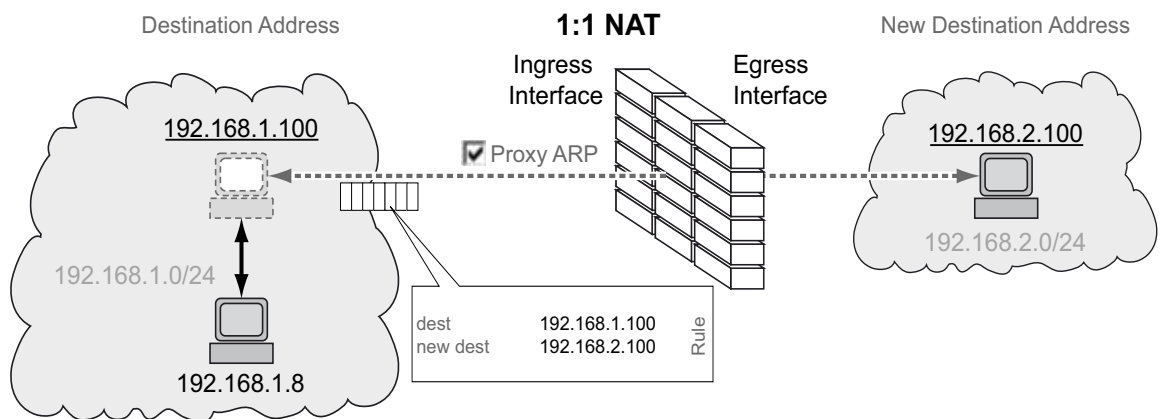
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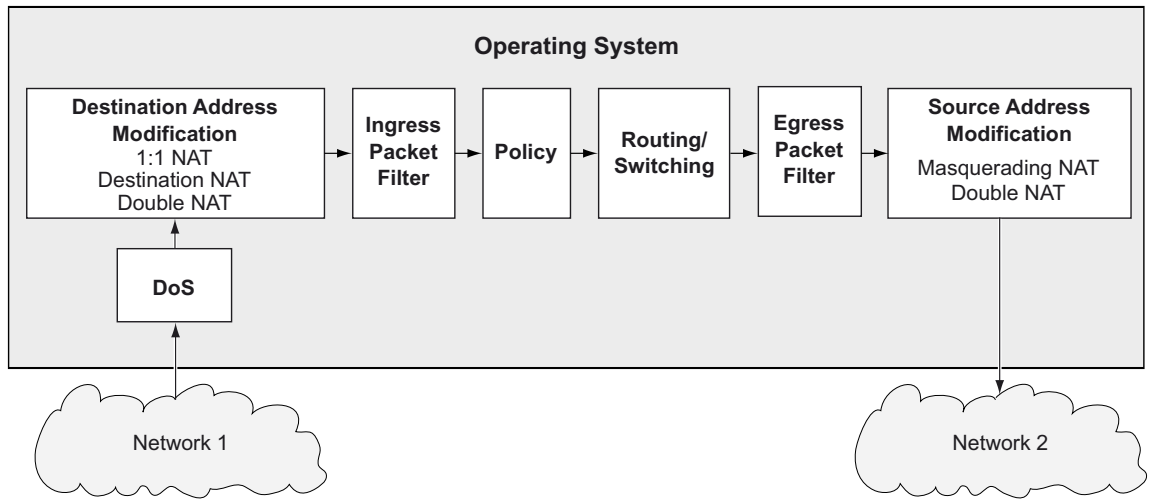












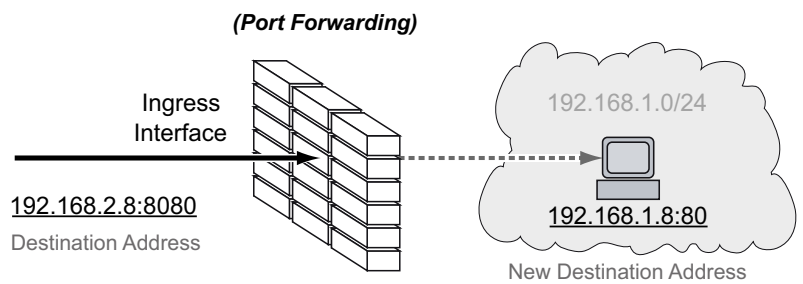
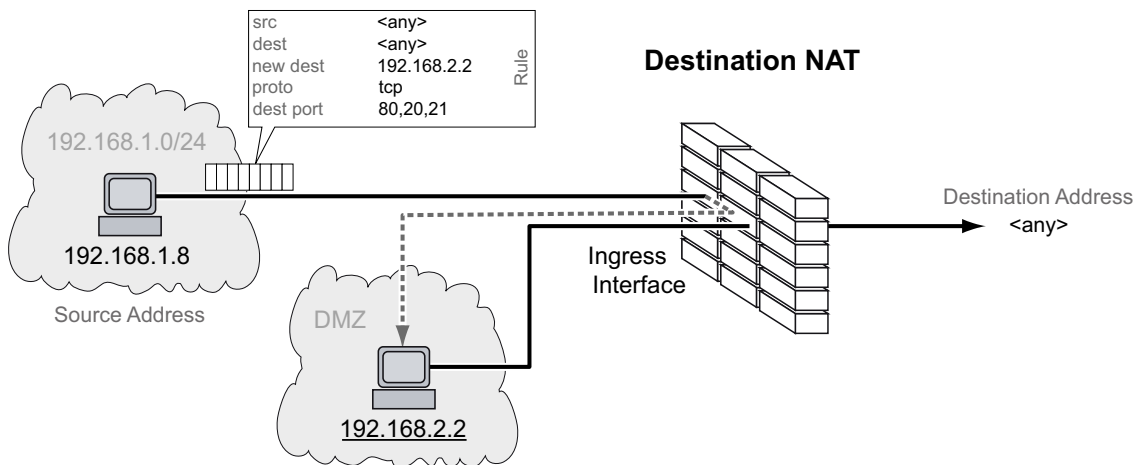
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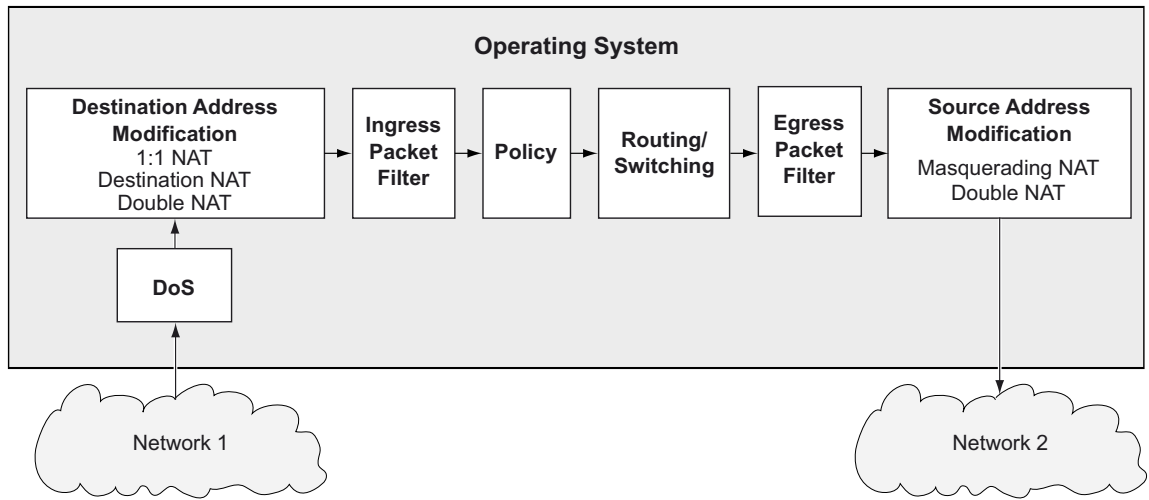
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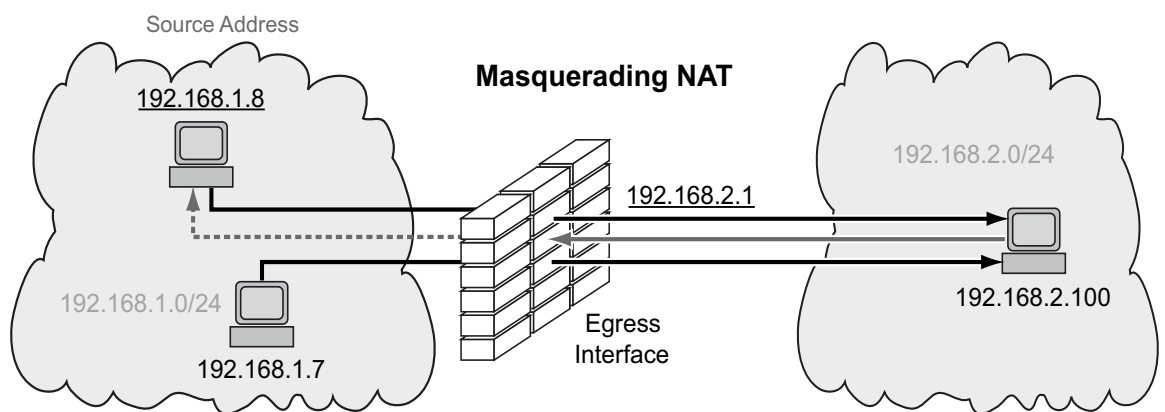


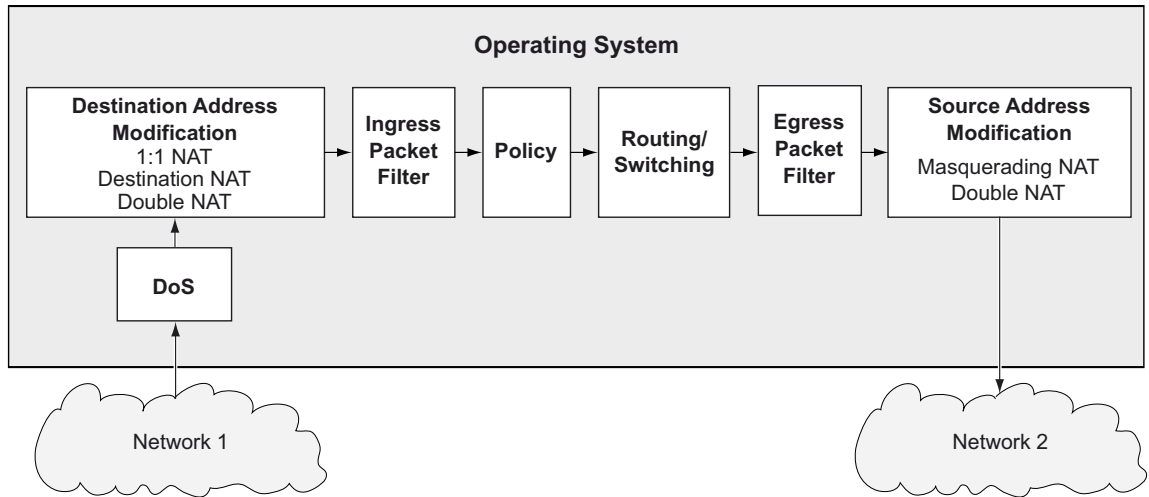














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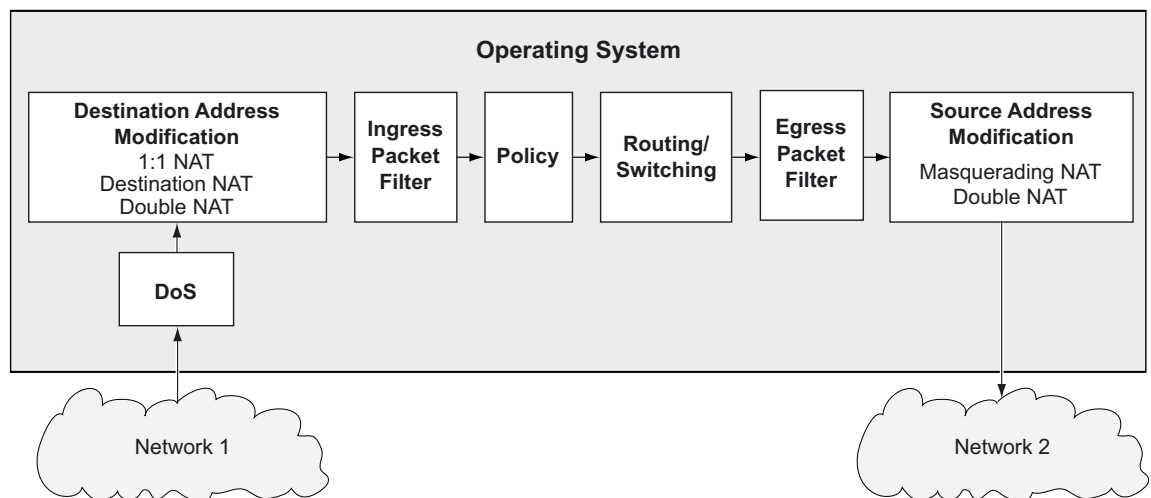
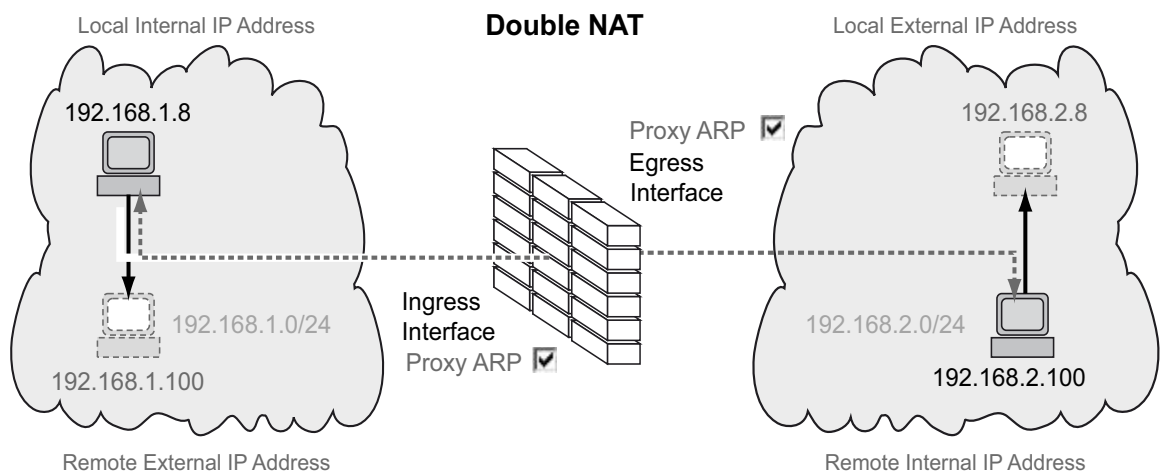
















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A series of horizontal black lines spaced evenly down the page, providing a template for text entry.

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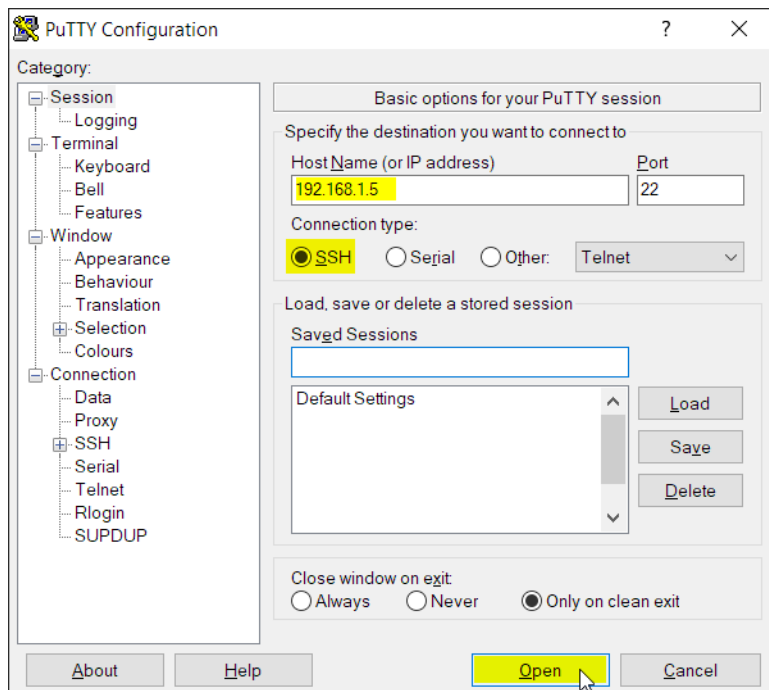


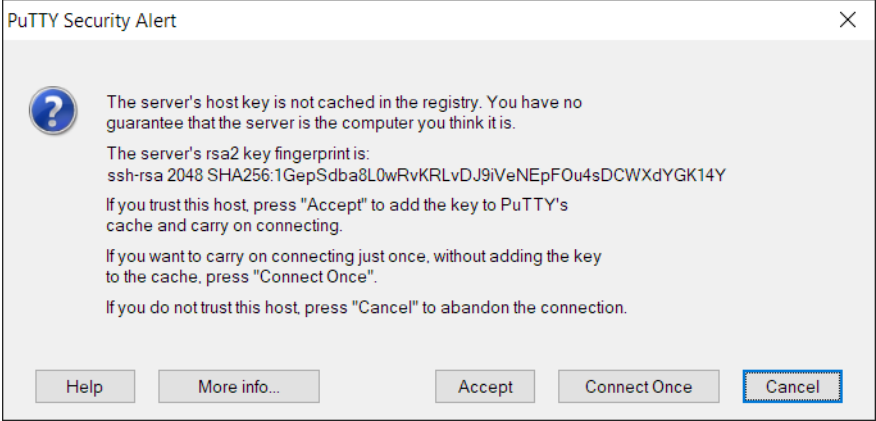




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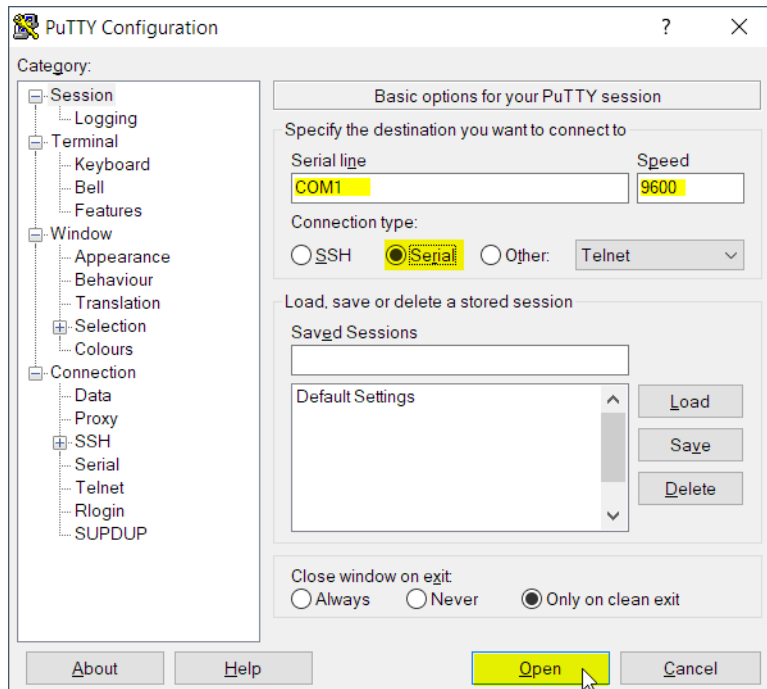
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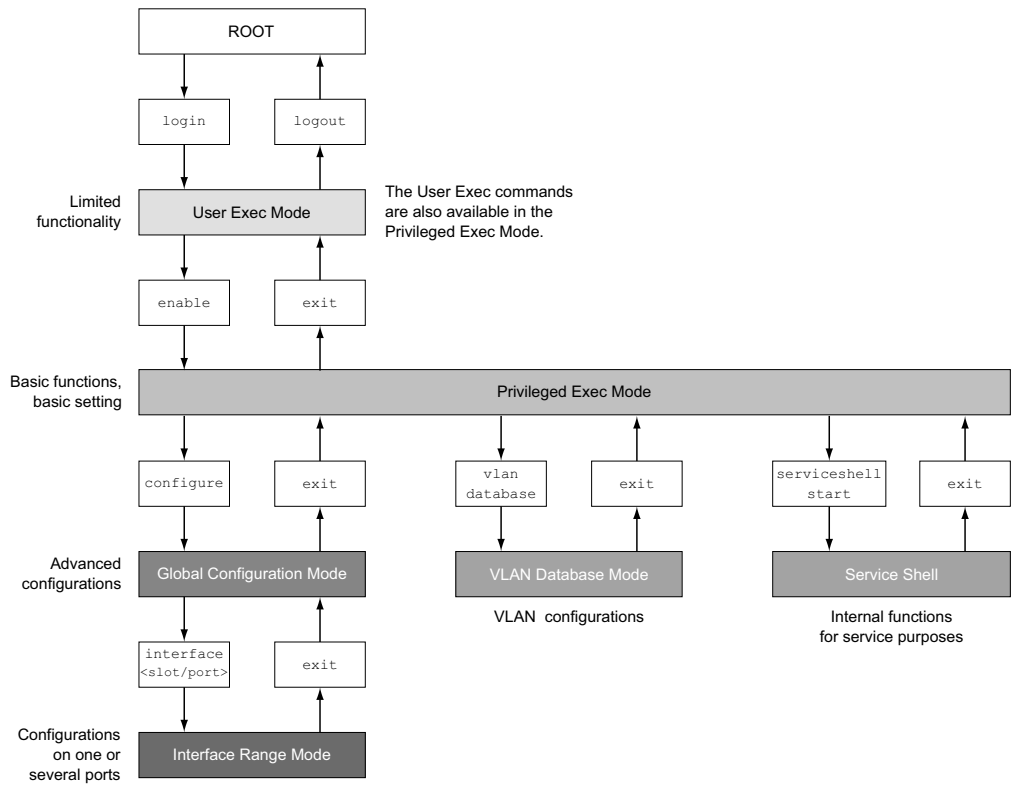
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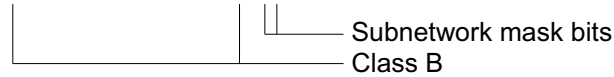
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0	Net ID - 7 bits	Host ID - 24 bits	Class A
1 0	Net ID - 14 bits	Host ID - 16 bits	Class B
1 1 0	Net ID - 21 bits	Host ID - 8 bits	Class C
1 1 1 0	Multicast Group ID - 28 bits		Class D
1 1 1 1	reserved for future use - 28 bits		Class E

Decimal notation  
255.255.192.0

Binary notation  
11111111.11111111.11000000.00000000



Decimal notation

129.218.65.17

└─── 128 < 129 191 > Class B

Binary notation

10000001.11011010.01000001.00010001

└─── Subnetwork 1  
└─── Network address

Decimal notation

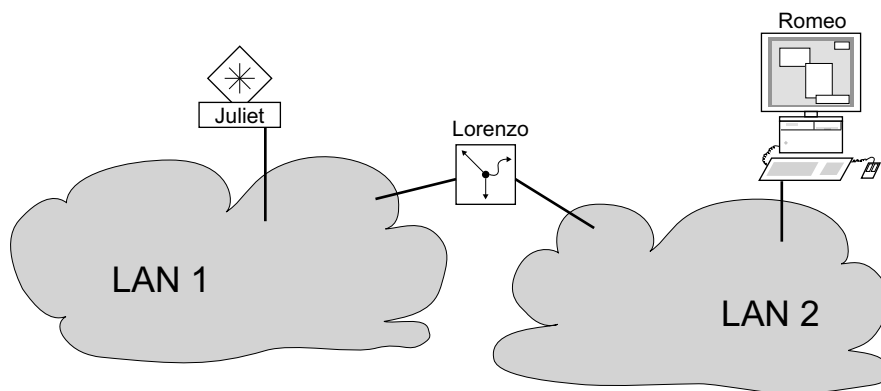
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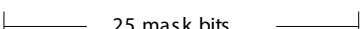
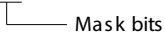
Binary notation

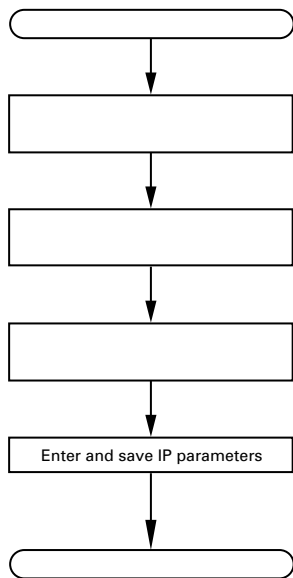
10000001.11011010.10000001.00010001

└─── Subnetwork 2  
└─── Network address





IP address, decimal	Network mask, decimal	IP address, binary
192.168.112.1	255.255.255.128	11000000 10101000 01110000 00000001
192.168.112.127		11000000 10101000 01110000 01111111
		 25 mask bits
CIDR notation: 192.168.112.0/25		
	 Mask bits	



```
NOTE: Enter '?' for Command Help. Command help displays all opt
that are valid for the particular mode.
For the syntax of a particular command form, please
consult the documentation.

! ( )>
```



Id	MAC Address	Writable	IP Address	Net Mask	Default Gateway	Product	Name
1	00:80:63:A4:CC:00	<input checked="" type="checkbox"/>	10.115.0.76	255.255.224.0	10.115.0.3		
2	00:80:63:C0:50:00	<input type="checkbox"/>	10.115.0.33	255.255.224.0	10.115.0.3		
3	00:80:63:A3:40:00	<input type="checkbox"/>	10.115.0.70	255.255.224.0	10.115.0.3		
4	00:80:63:98:14:00	<input type="checkbox"/>	10.115.0.17	255.255.224.0	10.115.0.3		
5	00:80:63:9E:E4:00	<input type="checkbox"/>	0.0.0.0	0.0.0.0	0.0.0.0		
6	00:80:63:46:00:06	<input checked="" type="checkbox"/>	192.168.2.181	255.255.255.0	192.168.2.1		
7	00:80:63:A3:40:40	<input type="checkbox"/>	10.115.0.59	255.255.224.0	10.115.0.3		
8	00:80:63:A4:CC:40	<input type="checkbox"/>	10.115.0.81	255.255.224.0	10.115.0.3		
9	00:80:63:6E:38:4E	<input checked="" type="checkbox"/>	192.168.2.174	255.255.255.0	192.168.2.1		
10	00:80:63:18:2A:61	<input checked="" type="checkbox"/>	192.168.2.170	255.255.255.0	192.168.2.1		
11	00:80:63:A3:40:80	<input type="checkbox"/>	10.115.0.66	255.255.224.0	10.115.0.3		
12	00:80:63:A4:CC:80	<input type="checkbox"/>	10.115.0.80	255.255.224.0	10.115.0.3		
13	00:80:63:61:AC:81	<input checked="" type="checkbox"/>	192.168.2.176	255.255.255.0	192.168.2.1		
14	00:80:63:98:10:95	<input type="checkbox"/>	10.115.0.22	255.255.224.0	10.115.0.3		
15	00:80:63:61:AC:AB	<input checked="" type="checkbox"/>	192.168.2.40	255.255.255.0	192.168.2.1		
16	00:80:63:3B:5C:BD	<input checked="" type="checkbox"/>	192.168.2.178	255.255.255.0	192.168.2.1		
17	00:80:63:A3:40:C0	<input type="checkbox"/>	10.115.0.72	255.255.224.0	10.115.0.3		
18	00:80:63:8F:2C:BE	<input type="checkbox"/>	10.115.0.40	255.255.224.0	10.115.0.3		
19	00:80:63:88:38:EC	<input checked="" type="checkbox"/>	192.168.110.92	255.255.255.0	0.0.0.0		
20	00:80:63:9B:11:00	<input type="checkbox"/>	10.115.0.35	255.255.224.0	10.115.0.3		
21	00:80:63:A4:CD:00	<input type="checkbox"/>	10.115.0.77	255.255.224.0	10.115.0.3		
22	00:80:63:99:41:08	<input type="checkbox"/>	10.115.0.13	255.255.224.0	10.115.0.3		
23	00:80:63:17:35:08	<input checked="" type="checkbox"/>	192.168.2.164	255.255.255.0	192.168.2.1		
24	00:80:63:44:19:2E	<input checked="" type="checkbox"/>	10.115.5.130	255.255.224.0	10.115.0.3		

**Properties** [X]

MAC Address: 00:80:63:A3:40:00

Name: Power Unit 1 Switch 2

IP Configuration

IP Address:  .  .  .

Net Mask:  .  .  .

Default Gateway:  .  .  .



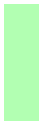
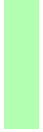




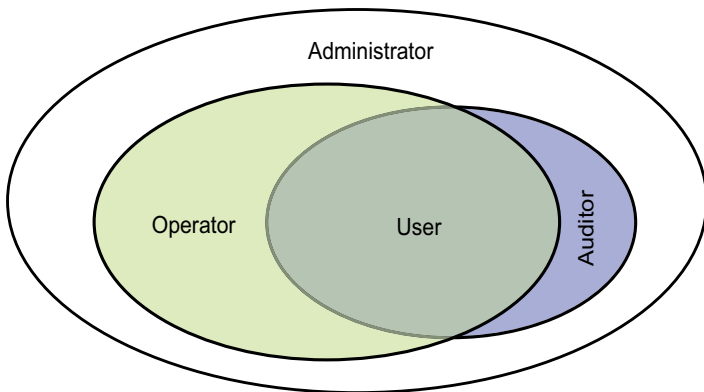


















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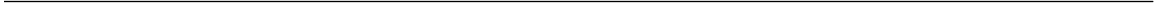
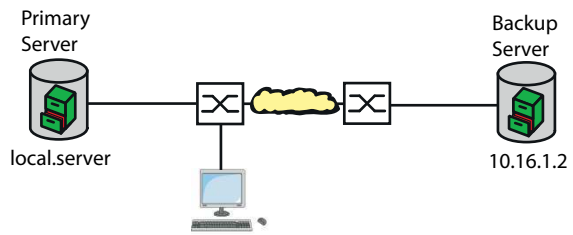




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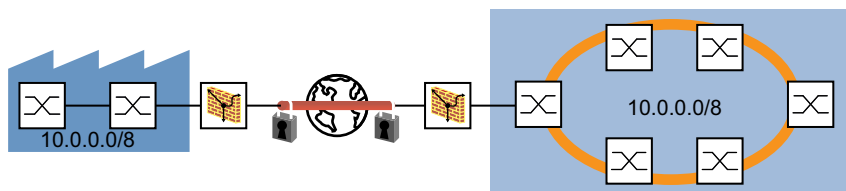
















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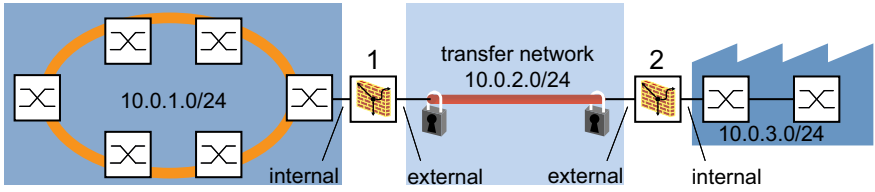
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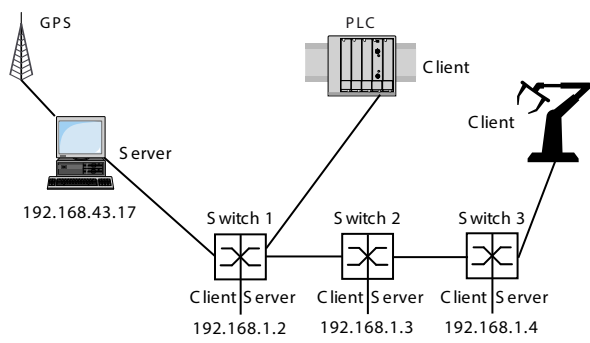












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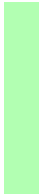
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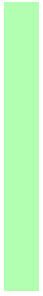




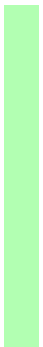


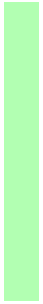






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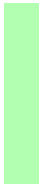




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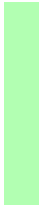






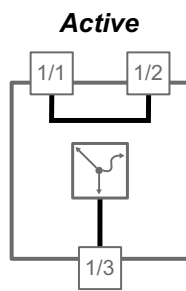
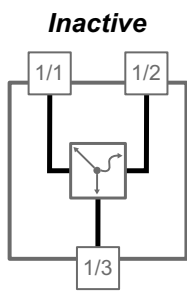








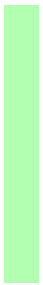




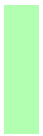
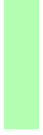


















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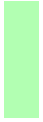

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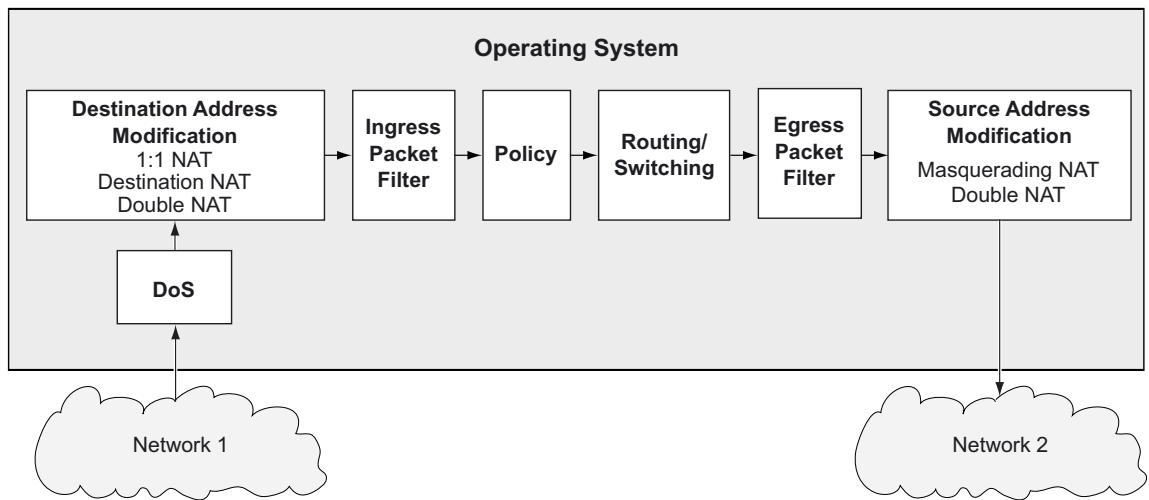
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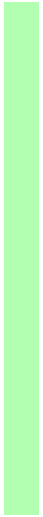






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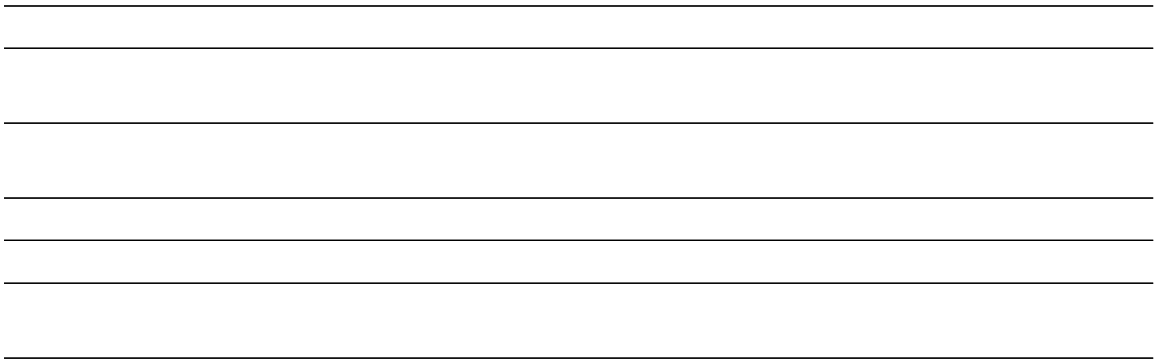




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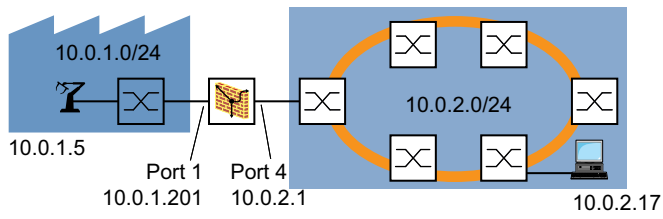

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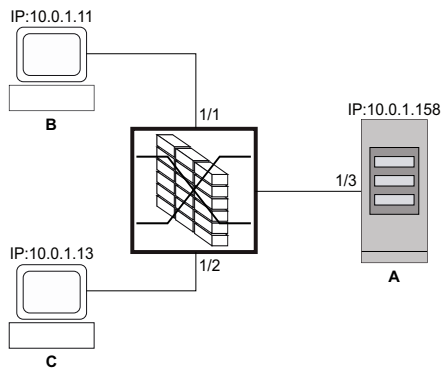










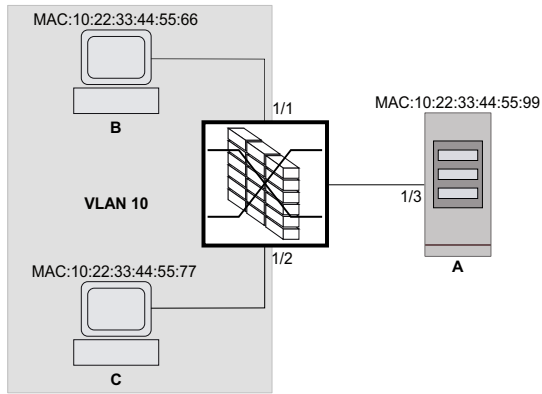






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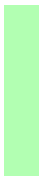
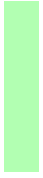




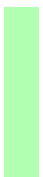
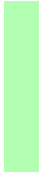




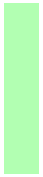
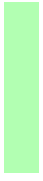








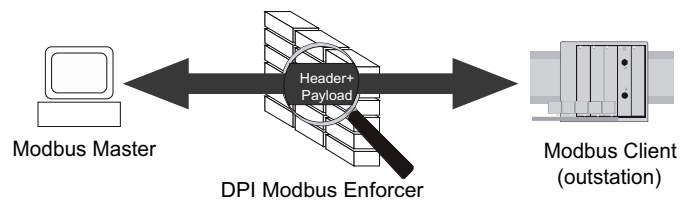










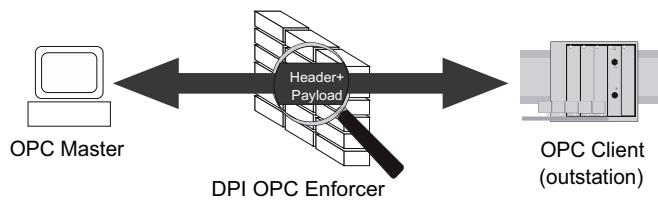




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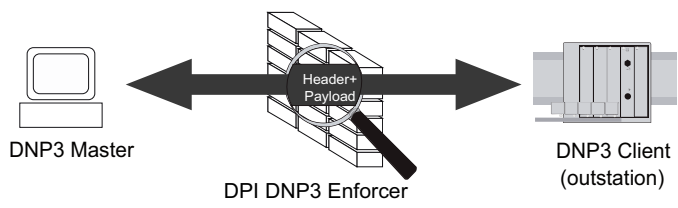


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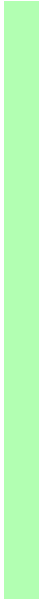
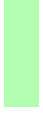


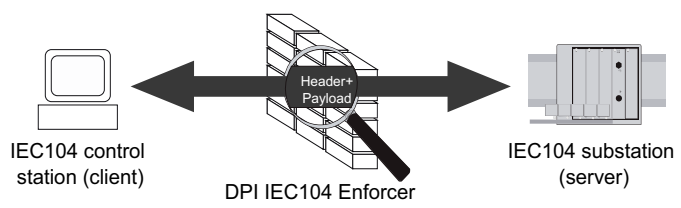


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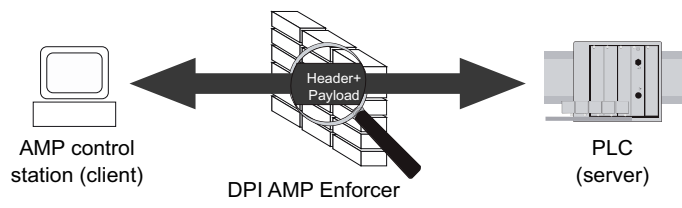
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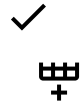
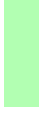




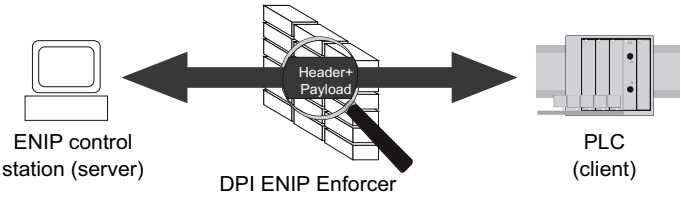


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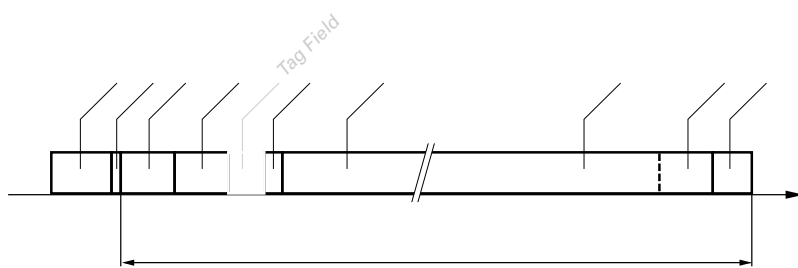
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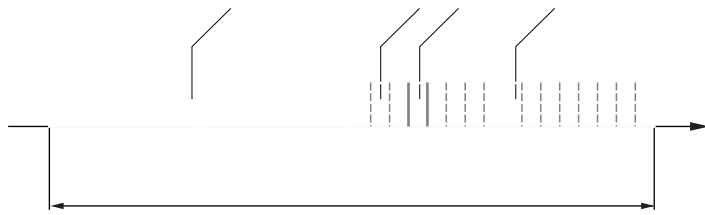




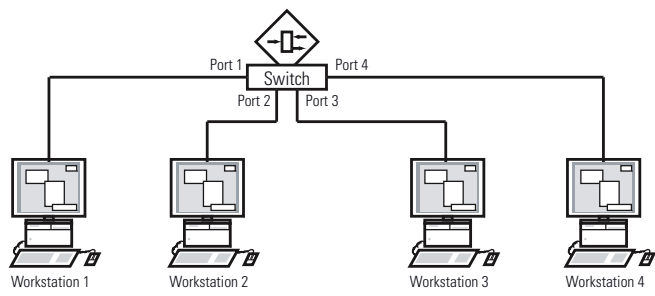








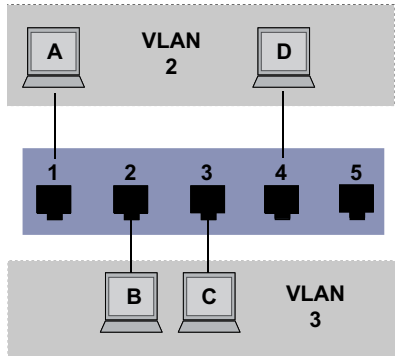












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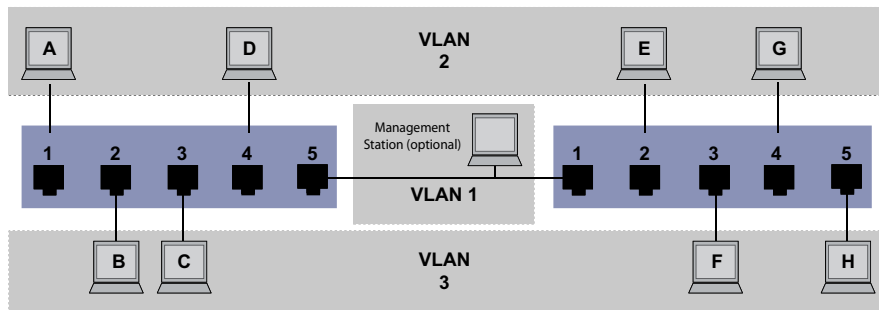
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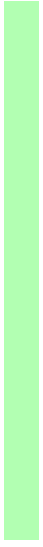














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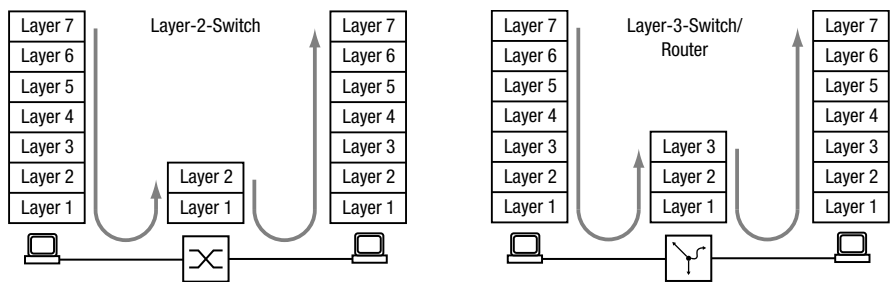
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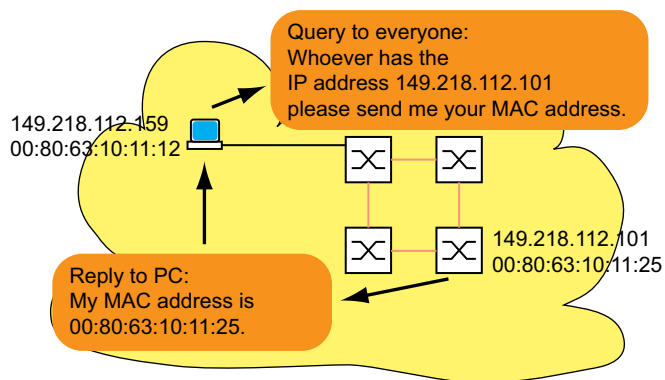
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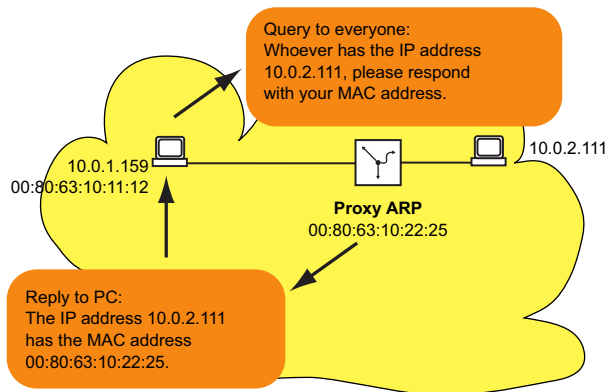
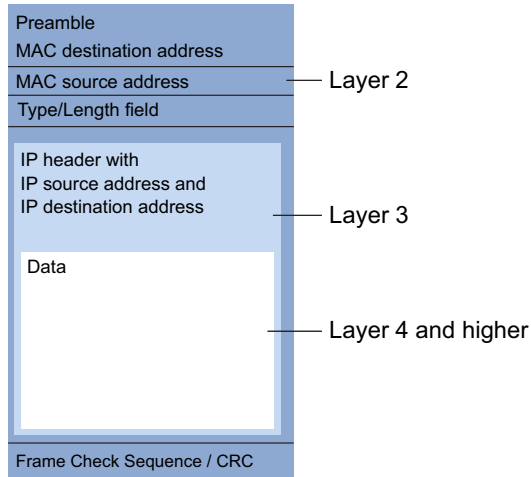
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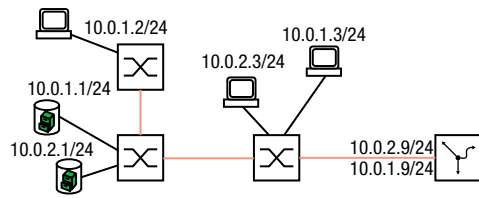
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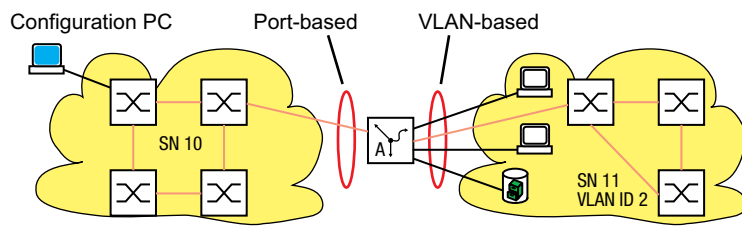
IP address, decimal	Network mask, decimal	IP address, binary
149.218.112.1	255.255.255.128	10010101 11011010 01110000 00000001
149.218.112.127		10010101 11011010 01110000 01111111
		----- 25 mask bits -----

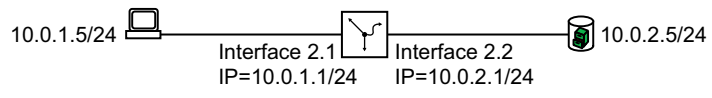
CIDR notation: 149.218.112.0/25

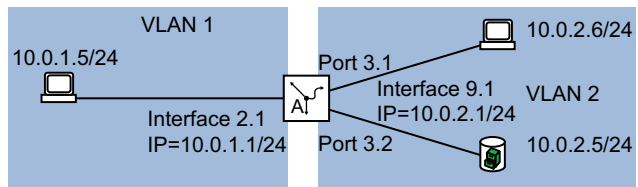
└----- Mask bits



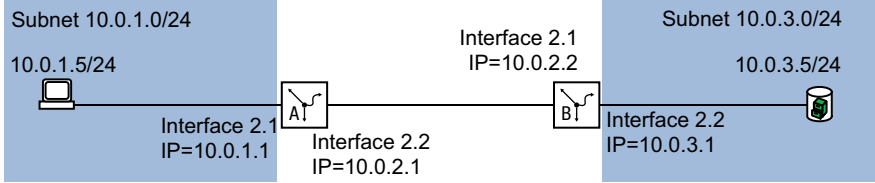


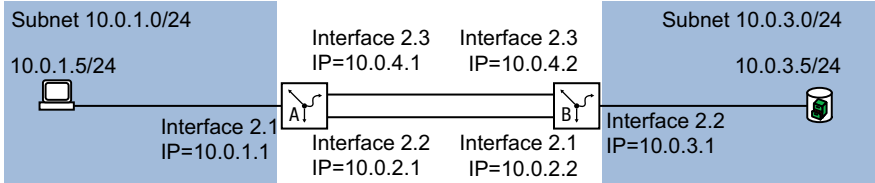






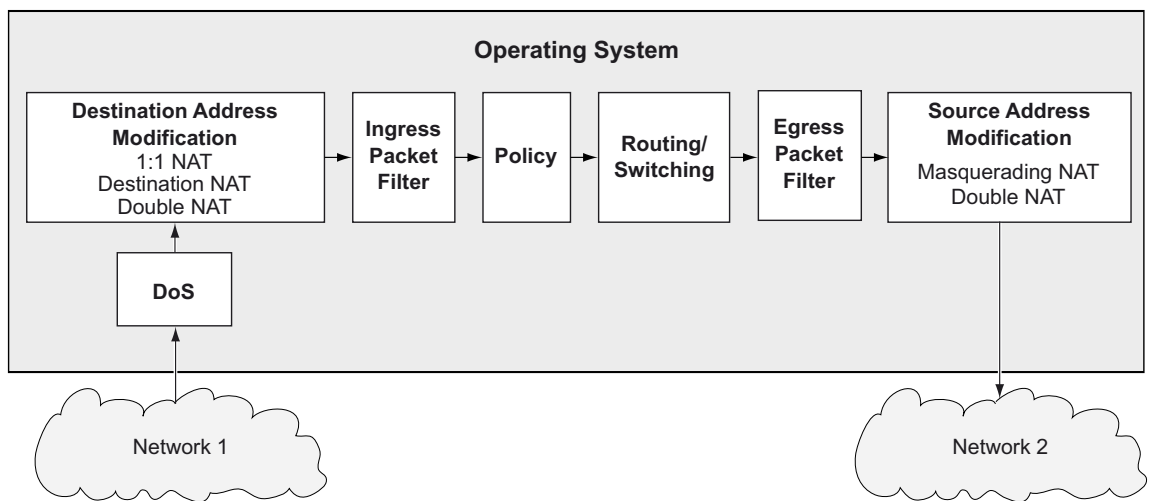


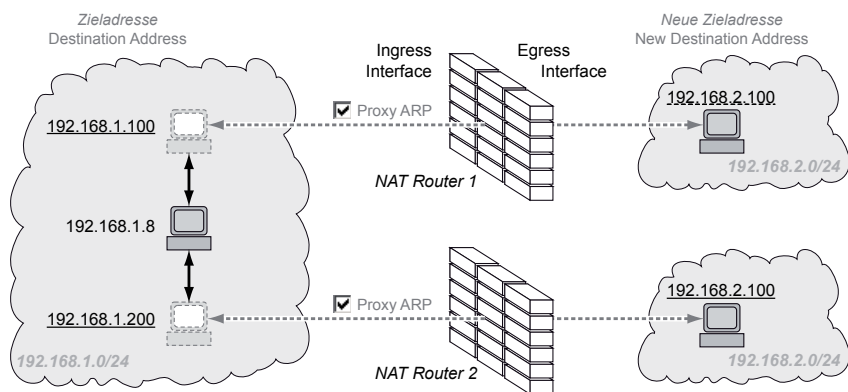
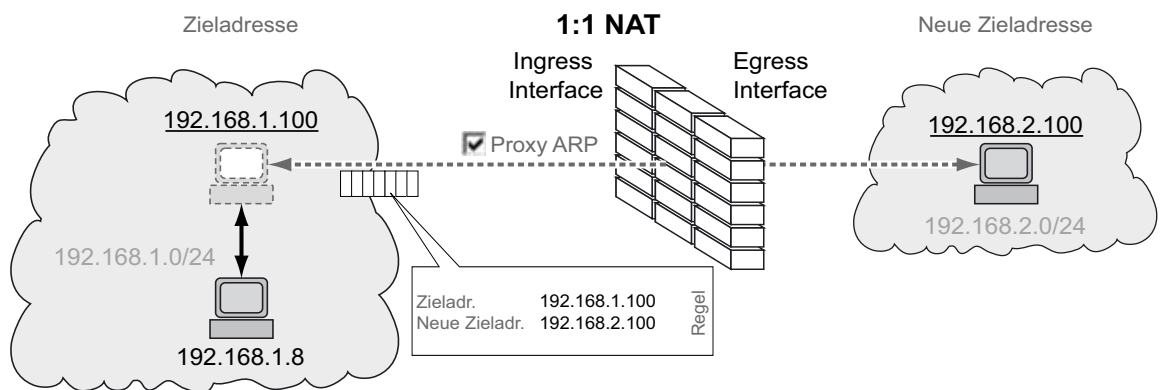






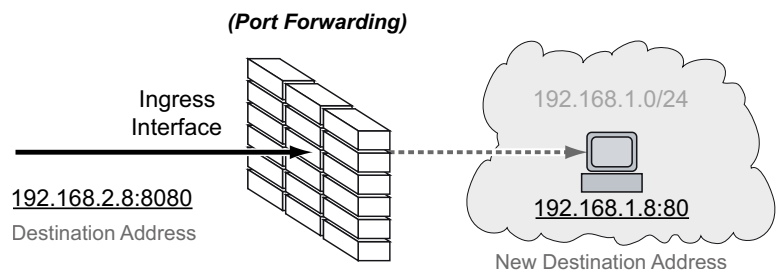
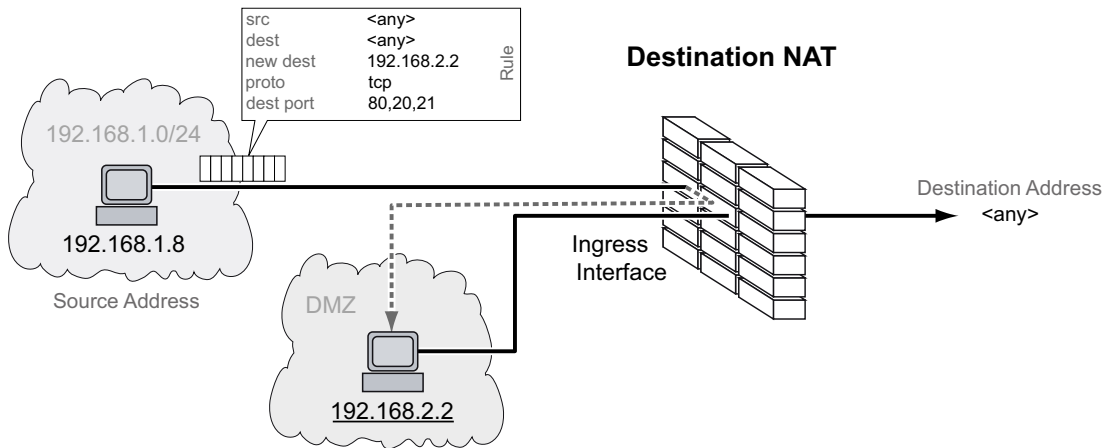













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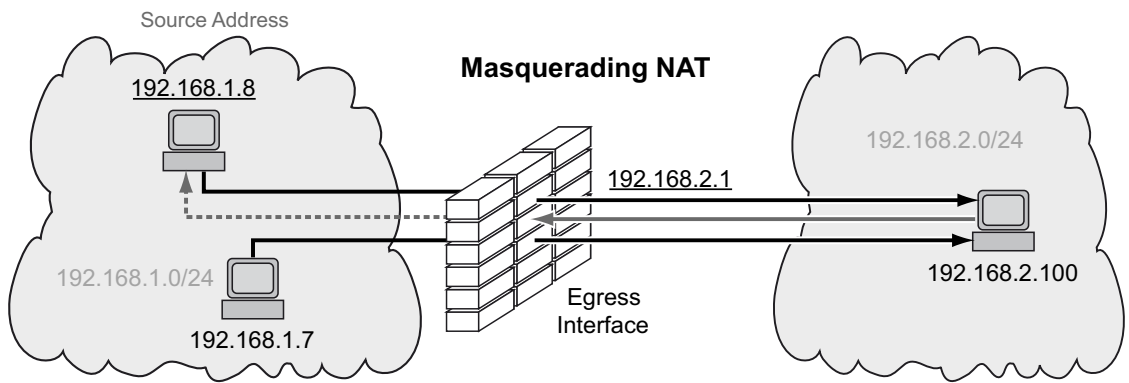
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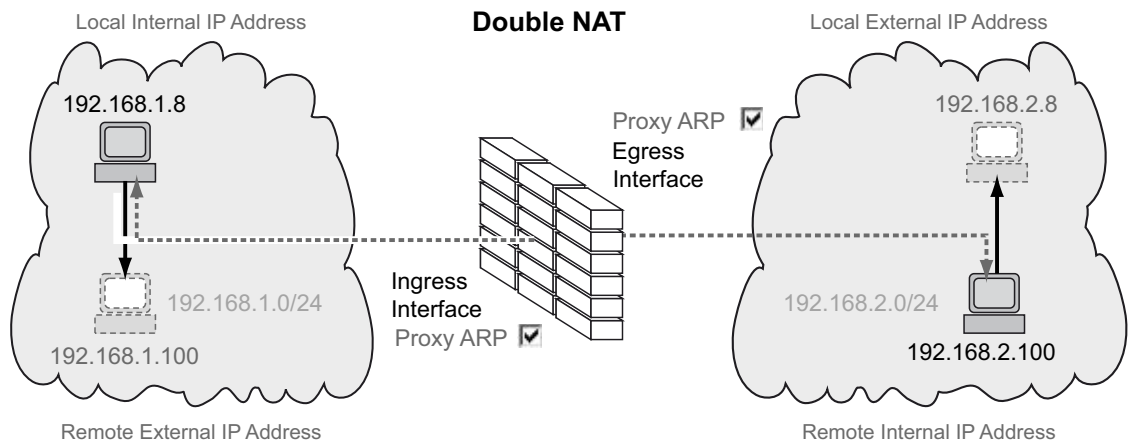


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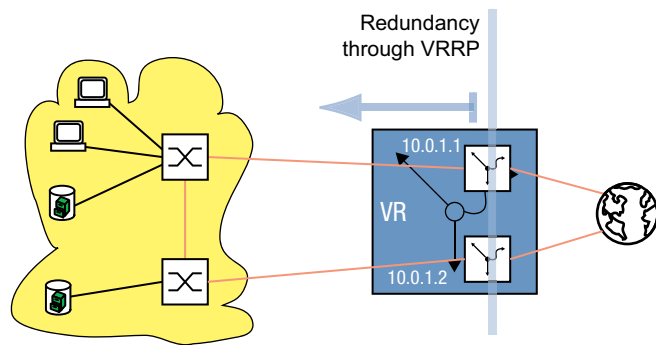












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00:00:5e:00:01:xx

variable element = VRID  
constant element

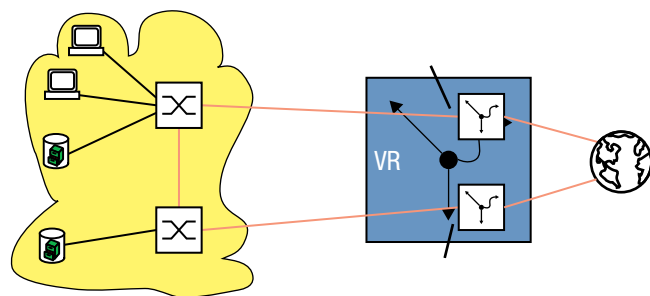
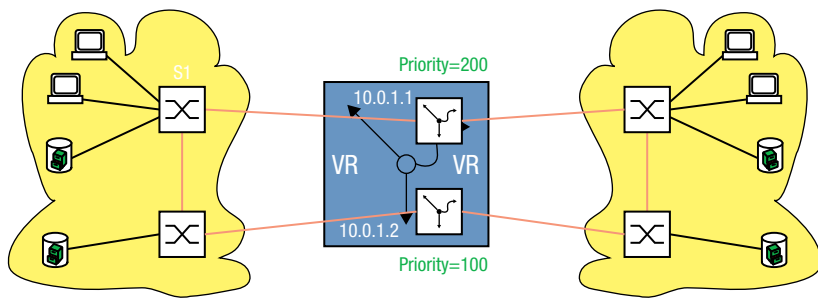
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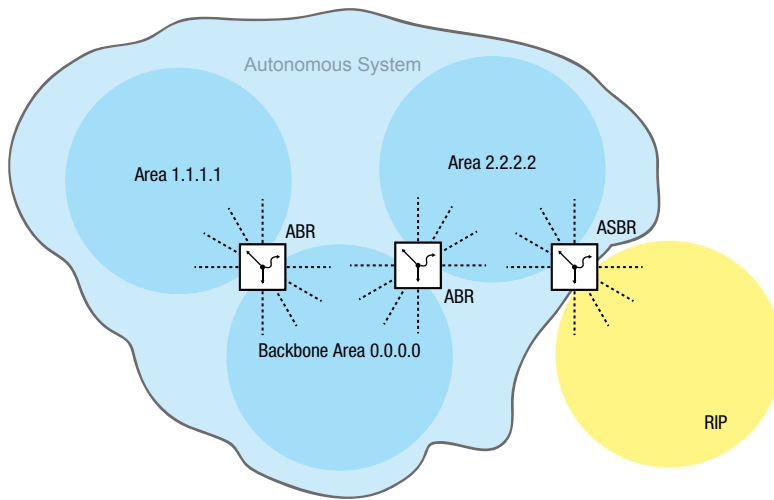


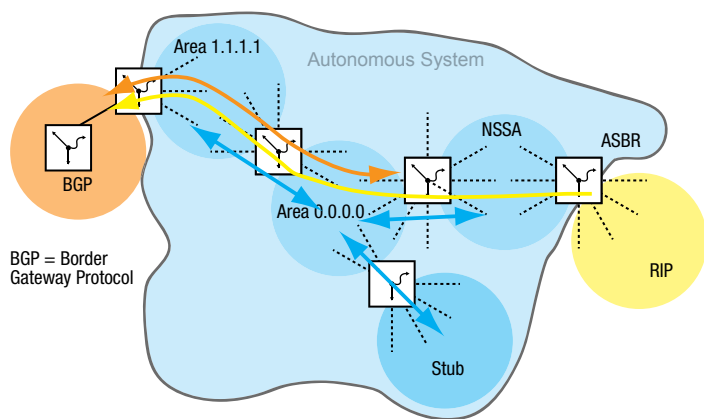


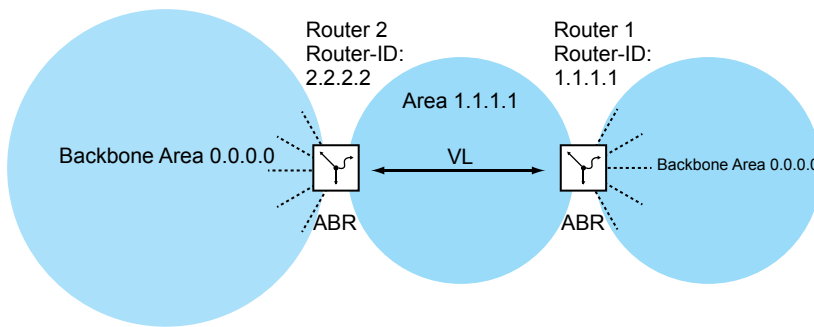
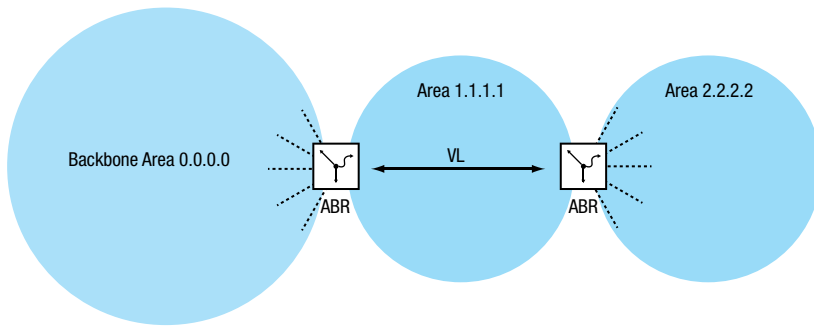


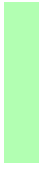




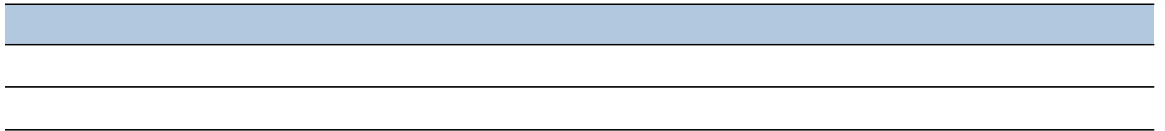
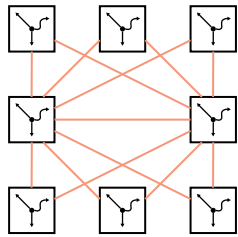
















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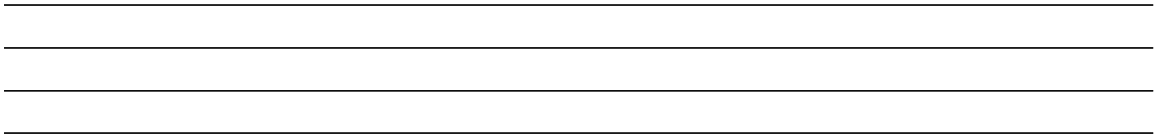
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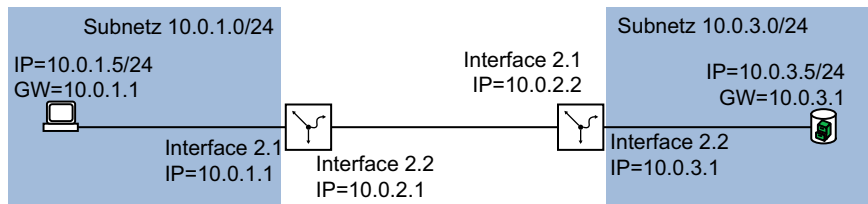
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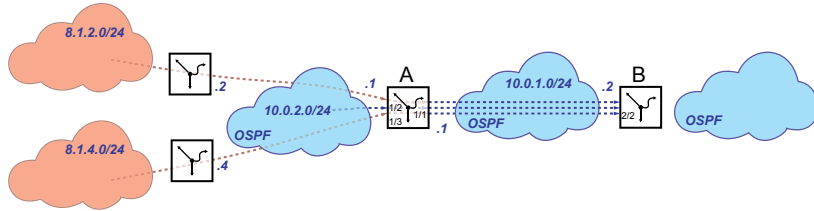




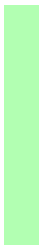
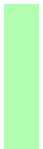
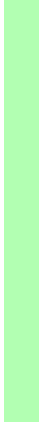
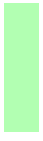




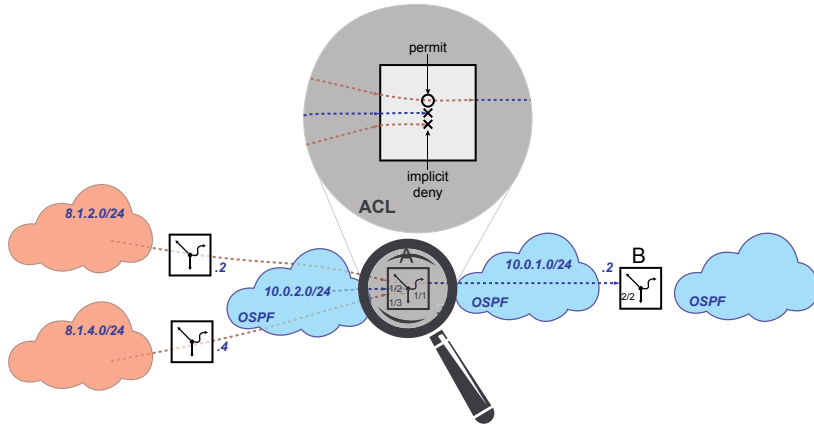




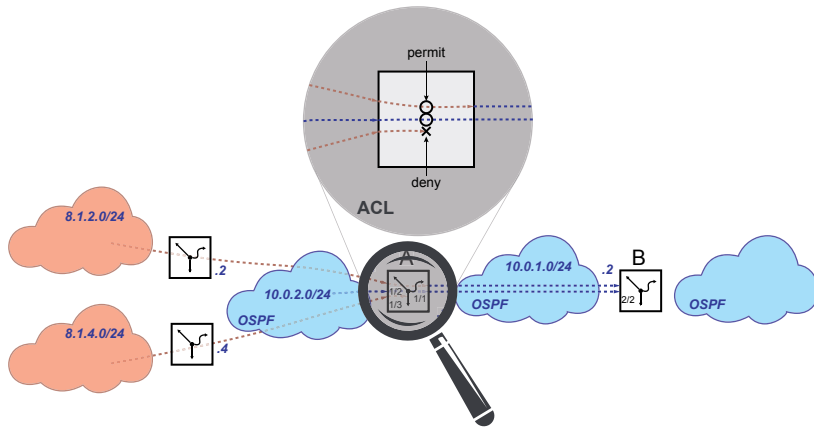


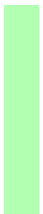
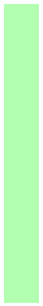
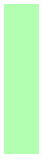








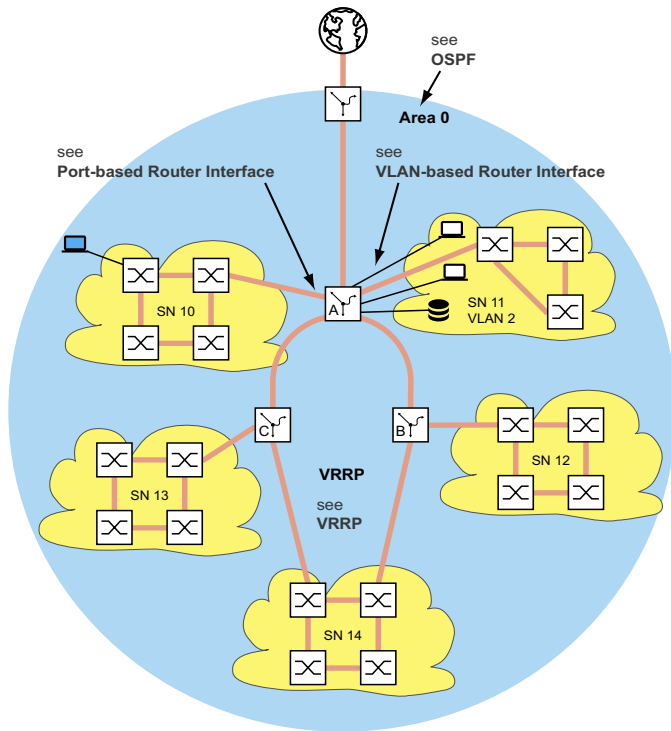




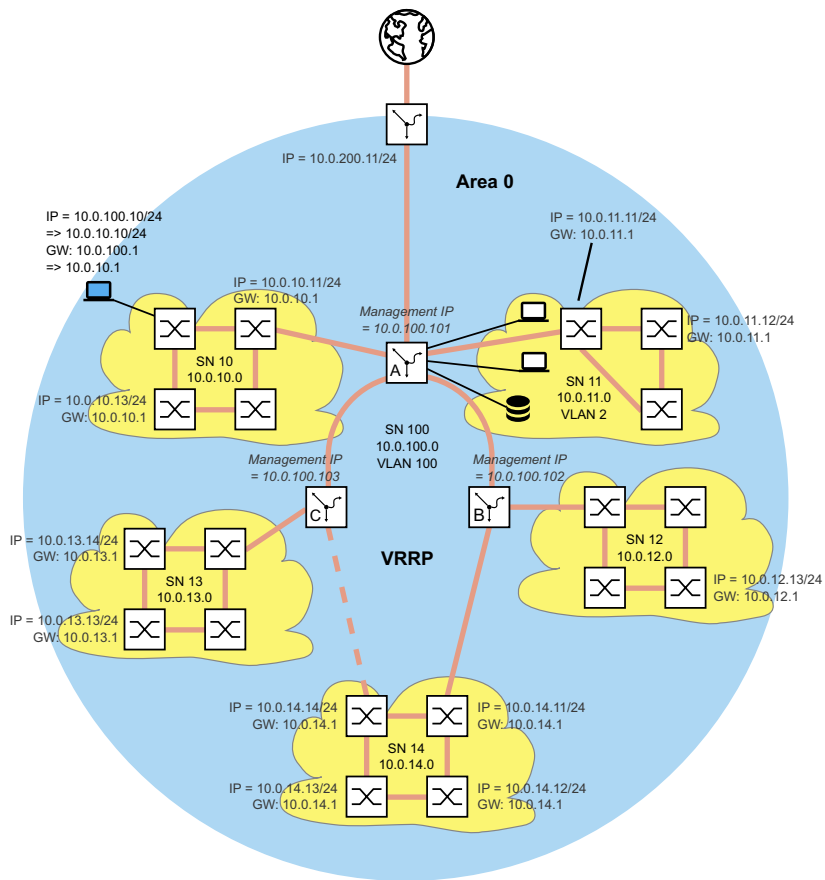


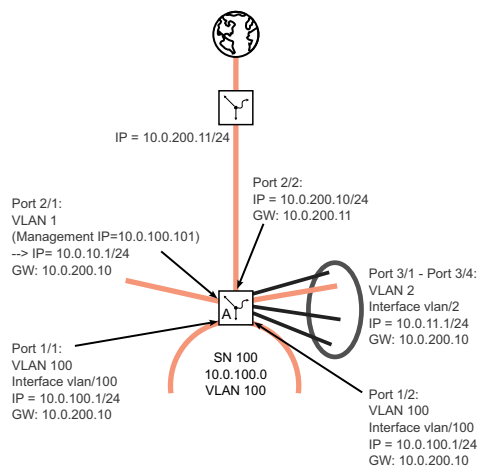




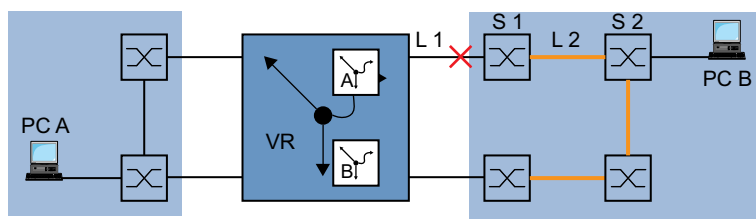




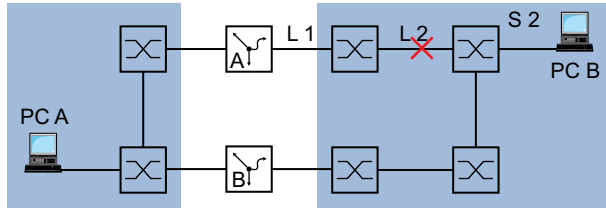
















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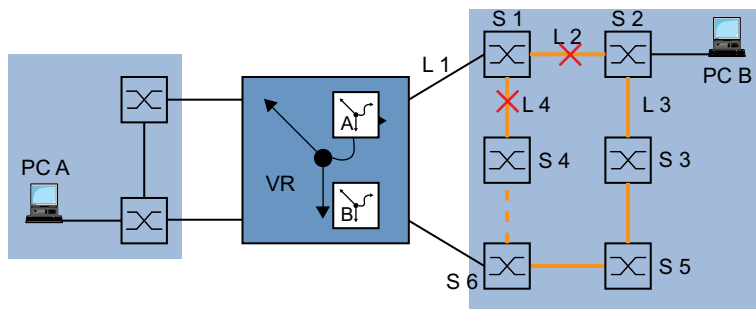




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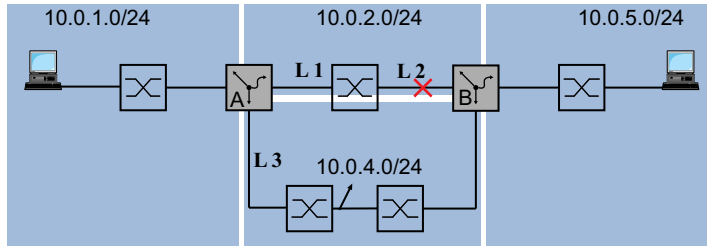




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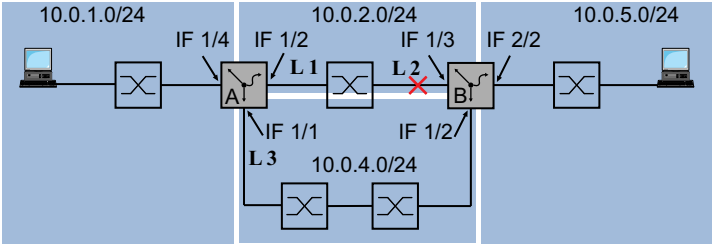
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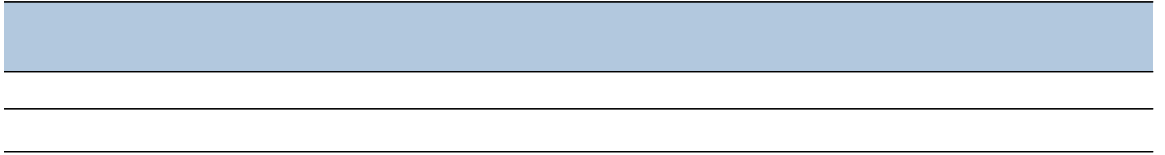




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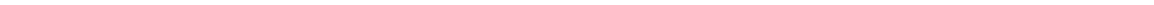
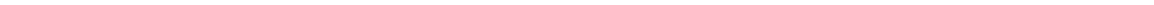
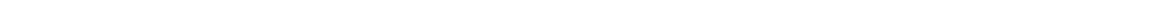
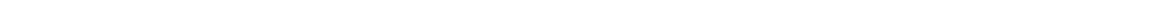
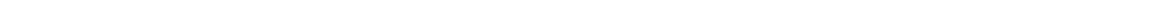
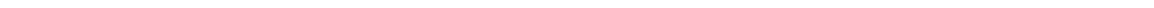
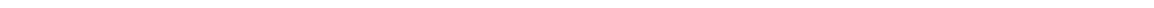
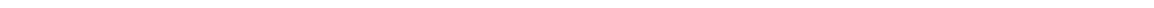
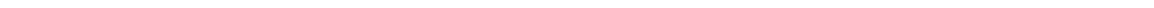
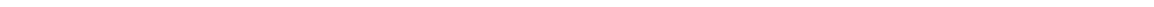
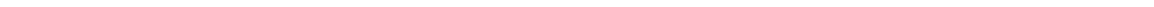
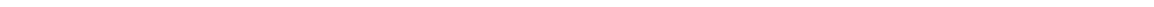
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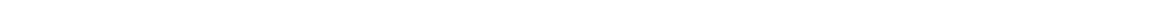




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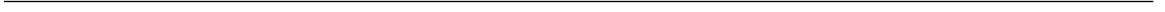




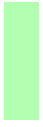








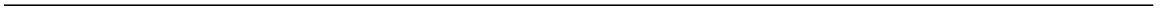


















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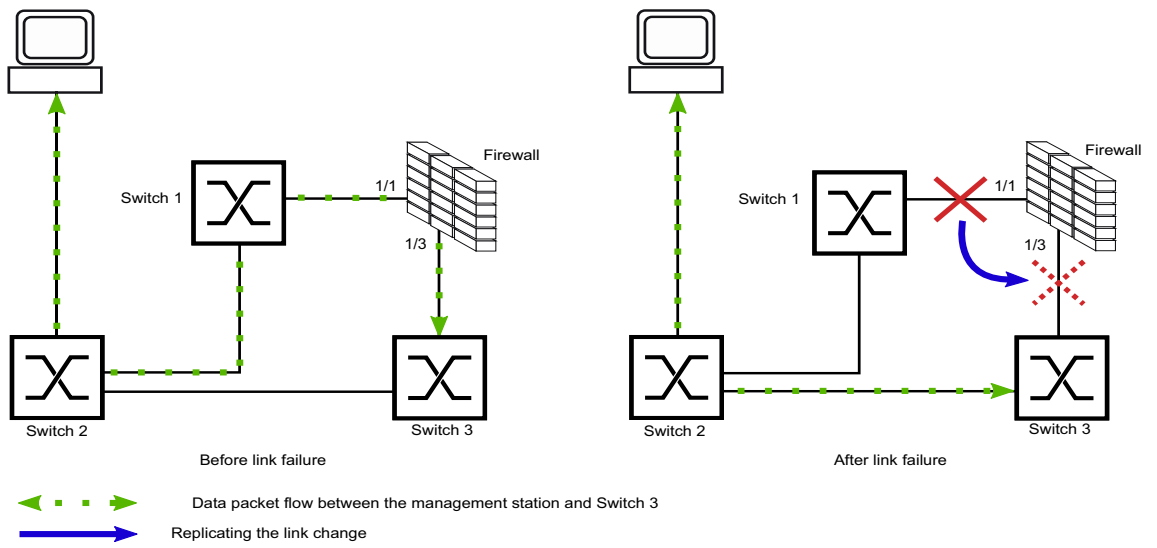
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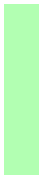
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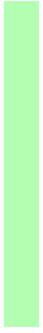


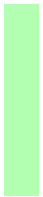
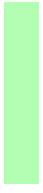




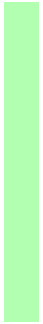
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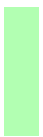
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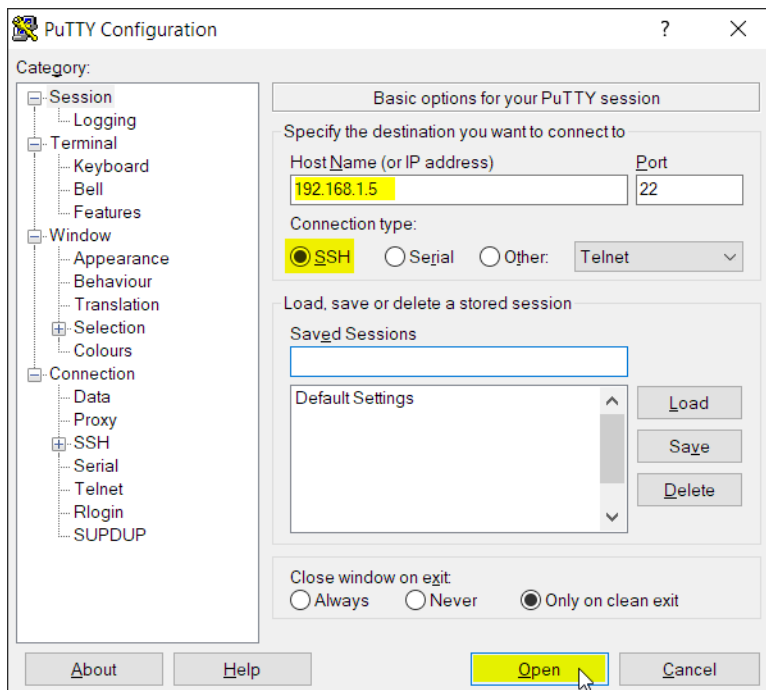


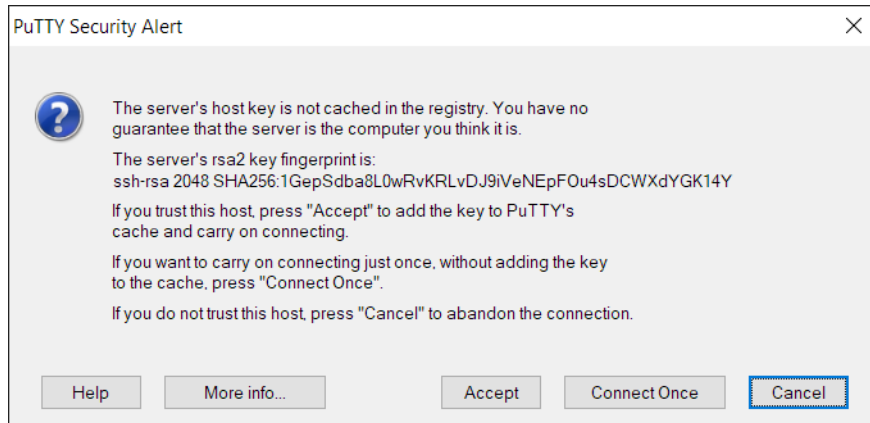
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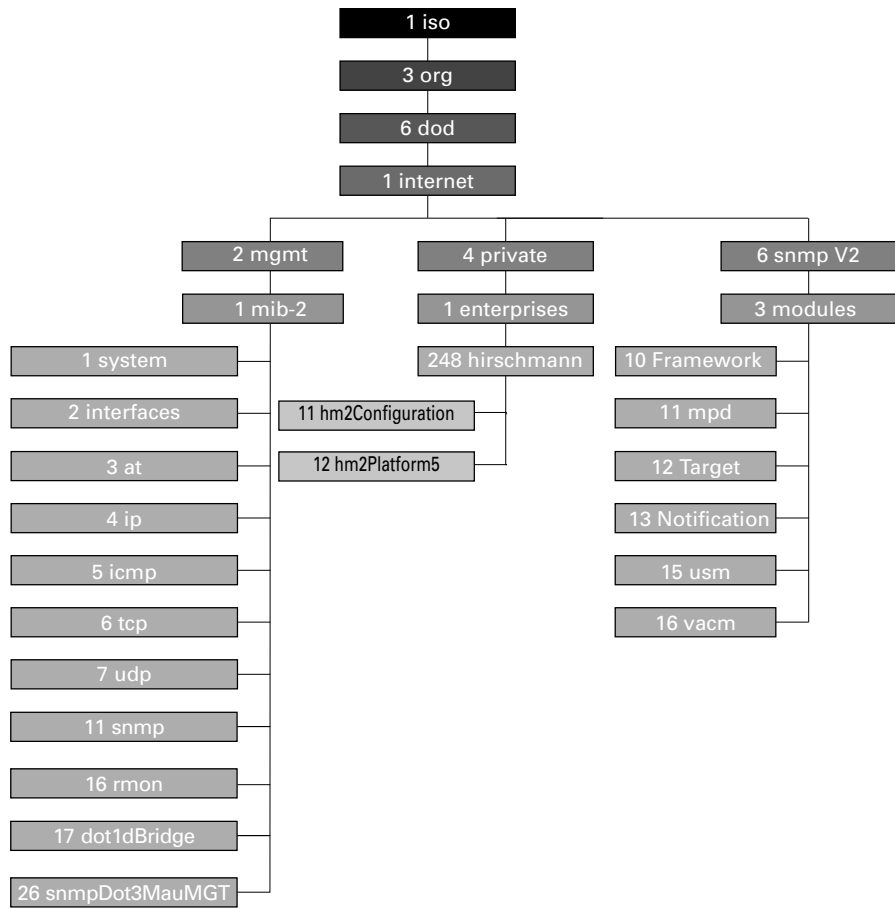
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A series of horizontal lines for writing, consisting of 25 evenly spaced lines.



















A series of horizontal black lines spaced evenly down the page, providing a template for text entry.

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**HIRSCHMANN**